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#161
Formal
Drawings
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DSS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shekhar Y. Art Unit: 2816
Borkar et al. Examiner: Cassandra Cox
Serial No. : 09/802,584 Assignee: Intel Corporation
Filed : March 8, 2001
Title : A SYNCHRONOUS CLOCK GENERATOR FOR INTEGRATED CIRCUITS

Attention: Official Draftsman
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RECEIVED
JUL 31 2003
TC 2800 MAIL ROOM

TRANSMITTAL OF FORMAL DRAWINGS

Please substitute the enclosed three (3) sheets of formal drawings for the corresponding drawings presently in the application.


Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

/BY
ALEX CHEN
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Date: 7/21/03

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I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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